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of

Complete if Known **Application Number** 10/684,713 Filing Date 10/14/2003 First Named Inventor Paul Arthur Layman Art Unit 2811 **Examiner Name** Unassigned Chaudhry 25-18-8-12-5/075903-282 Attorney Docket Number

				DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number  Number-Kind Code <sup>2 (8 known)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevan Figures Appear
RA	Α	<sup>US-</sup> 6475866	11-05-2002	Hofmann, et al	
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Examiner Cite Initials* No.		Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages
		Country Code <sup>3</sup> Number <sup>4</sup> 'Kind Code <sup>8</sup> (if known)	MM-DD-YYYY	· · · · · · · · · · · · · · · · · · ·	Or Relevant Figures Appear

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Substitut	te for form 1449/PTC	`		Complete if Known		
	SUPPLEMENTAL			Application Number	10/684,713	
INF	DRMATIO	N DIS	CLOSURE	Filing Date	10/14/2003	
STA	STATEMENT BY APPLICANT			First Named Inventor	Paul Arthur Layman	
	(lles se many e	hants on n	ncaccend	Art Unit	2811	
	(Use as many sheets as necessary)			Examiner Name	Unassigned	
Sheet	2	of	2	Attorney Docket Number	Chaudhry 25-18-8-12-5/075903-282	

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
RP	1	DUDEK, ET AL, "Lithography-Independent Nanometer Silicon MOSFET's on Insulator", IEEE Transactions on Electron Devices, Vol. 43, No. 10, October 1996, pp. 1626-1631.	
RP	2	RISCH, ET AL, "Vertical MOS Transistors with 70 nm Channel Length", IEEE Transactions on Electron Devices, Vol. 43, No. 9, September 1996, pp. 1495-1498.	
RP	3	TAKATO, ET AL, "Impact of Surrounding Gate Transistor (SGT) for Ultra-High-Density LSI's", IEEE Transactions on Electron Devices, Vol. 38, No. 3, March 1991, pp. 573-577.	
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Examiner Signature	fon	lompey	Date Considered	6-28-13

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## **ELECTRONIC INFORMATION DISCLOSURE STATEMENT**

Electronic Version v18 Stylesheet Version v18.0

> Title of Invention

Multiple Operating Voltage Vertical Replacement-Gate (VRG) Transistor

**Application Number:** 

10/684713

Confirmation Number:

4836

First Named Applicant:

Paul Layman Attorney Docket Number: 075903-282

Art Unit:

2811

Search string:

( 4670768 or 5010386 or 5140388 or 5502009 or 5861347 or 6300199 or 6309930 or 6387758 or 4366495 or 4455565 or 4587713 or 4683643 or 4786953 or 4837606 or 5342797 or 6027975 or 6072216 or 6133099 or 6197641 or 6133164 or 5414289 or 5576238 or 5668391 or 5744846 or 6297531 or 4951102 or 5208172 or 5545586

or 5554870 or 5864158 or 6268621 ).pn.

## **US Patent Documents**

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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## Signature

Examiner Name	Date
Ron Rompey	6-28\$05